Syllabus ECE113A, Section A Fall 2003 Code 15400 Electronics I

Textbook:	Semiconductor Device Fundamentals, Robert F. Pierret, Addison-			
Prerequisites:	Wesley, 1996. Physics 7E, ECE70A, and ECE70B			
What I want you to learn about ("Outcomes"):				
	1.	. Basic properties of semiconductors		
	2.	Carrier transport in semiconductors		
	3.	p-n junction diodes and bipolar junction transistors		
	4.	Transistor equivalent circuits and single stage amplifiers How to design:		
	5.			
		a.	doping profiles of basic p-n jun bipolar junction transistors	ction diodes, basic
		b.	single-stage transistor amplifier	S
Lecture Hours: Lecture Classroom: Discussion Sessions:	10:00 - 10:50 A.M. M, W & F IERF 101 A1: Tu 11:00-11:50 A.M. ICF 103 A2: F 8:00-8:50 A.M. IERF B015 (note change) A3: W 9:00-9:50 A.M. IERF B015 (note change)			
Instructor:	Peter Burke, Prof. of Electrical Engineering and Cptr. Science 2232 Engineering Gateway 949-824-9326 <u>pburke@uci.edu</u>			
Instructor Office Hours:	11-12 noon, M, W and F			
Lab quiz Teaching Assistants:	Monday, Dec. 1 (10 th week) Shengying Chen shengyic@uci.edu Kim Pham nkpham20@yahoo.com			
T.A. Office:	MSTB 216			
T.A. Office Hours:	Shengying Chen Tuesday 12-2 P.M. Thursday 11-1 P.M. Kim Pham: Monday 4-5 P.M., Tuesday 3-5 P.M., Thur. 4-5 P.M.			
Grading Components:	Midte	Homework 10% Midterm Exams (Oct. 20 and Nov. 10) 20% + 20% Final Exam (Dec. 8, 10:30am-12:20pm) 50%		