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Student ID \#:

## EECS 170A Section B

## Homework \#4

HW will be collected in DISCUSSION ONLY.
Do not turn your HW in anywhere else, or it will not be accepted. You are encouraged to turn it in at your own discussion section.

You may turn it in at any discussion section.
Last option to turn in: Right after Friday discussion section December 1, 2006. DUE: 10 AM Friday, December 1, 2006.
Please staple this sheet to the front of your homework.

| 1 | 2 a | 2 b | 2c | 2 ld | 2 e | 2 f | 2 g | 2 h | 2 i | Total |
| :--- | ---: | ---: | ---: | ---: | ---: | ---: | ---: | ---: | ---: | :---: |
| $/ 46$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 6$ | $/ 100$ |

1) In class we found:

$$
I=I_{0}\left(e^{q V_{\text {diode }} / k T}-1\right)
$$

Take $\mathrm{I}_{0}=10^{-14} \mathrm{~A}$. For the circuit shown, fill in the following table:


| $\mathrm{V}_{\mathrm{AD}}(\mathrm{V})$ | $\mathrm{V}_{\text {diode }}(\mathrm{V})=\mathrm{V}_{\mathrm{BC}}$ | $\mathrm{I}_{\mathrm{AD}}(\mathrm{A})$ |
| :--- | :--- | :--- |
| 0 |  |  |
| 0.5 |  |  |
| 1 |  |  |
| 1.5 |  |  |
| 2 |  |  |
| 2.5 |  |  |
| 3 |  |  |
| 3.5 |  |  |
| 4 |  |  |
| 4.5 |  |  |
| 5 |  |  |
| 5.5 |  |  |
| 6 |  |  |
| 6.5 |  |  |
| 7 |  |  |
| 7.5 |  |  |
| 8 |  |  |
| 8.5 |  |  |
| 9 |  |  |
| 9.5 |  |  |
| 10 |  |  |

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2) For the circuit shown below, find $\mathrm{I}_{\mathrm{B}}, \mathrm{I}_{\mathrm{E}}, \mathrm{I}_{\mathrm{C}}, \mathrm{V}_{\mathrm{C}}, \mathrm{V}_{\mathrm{B}}, \mathrm{V}_{\mathrm{E}}, \mathrm{V}_{\mathrm{BE}}, \mathrm{V}_{\mathrm{CE}}, \mathrm{V}_{\mathrm{BC}}$ defined in figure 10.2 (which one, a or b ?) of the text. Hints: the BE voltage drop is about 0.6 V . Take $\beta=100$. Then $\mathrm{I}_{\mathrm{C}}=100 \mathrm{I}_{\mathrm{B}}$. The rest is just applications of Kirchoff's current and voltage laws. Is the transistor biased in active mode?


Assuming the transistor is in active mode, find the following quantities in term of $\mathrm{R}_{1}, \mathrm{R}_{2}, \mathrm{R}_{3}, \mathrm{~V}_{1}$, and $\mathrm{V}_{2}$.

| $\mathrm{I}_{\mathrm{E}}=$ |  |
| :---: | :--- |
| $\mathrm{I}_{\mathrm{B}}=$ |  |
| $\mathrm{I}_{\mathrm{C}}=$ |  |
| $\mathrm{V}_{\mathrm{E}}=$ |  |
| $\mathrm{V}_{\mathrm{B}}=$ |  |
| $\mathrm{V}_{\mathrm{C}}=$ |  |
| $\mathrm{V}_{\mathrm{BE}}=$ |  |
| $\mathrm{V}_{\mathrm{CE}}=$ |  |
| $\mathrm{V}_{\mathrm{BC}}=$ |  |
|  |  |

Extra credit: If you are so inclined, can you find a requirement on $R_{1}, R_{2}, R_{3}, V_{1}$, and $V_{2}$.such that the transistor is in active mode?

