

**Syllabus**  
**EECS 270A Fall 2008 Code 18470**  
**Advanced Analog IC Design, I**

**Textbook:** *Design of Analog CMOS Integrated Circuits* Behzad Razavi McGraw Hill, 2001.

**Prerequisites:** EECS170C and 170LC, or equivalent; or consent of instructor.

**Lecture Hours:** 3:30-4:50 P.M. M/W  
**Lecture Classroom:** PCB 1300

**Instructor:** Peter Burke, Prof. of Electrical Engineering and Cptr. Science  
 2232 Engineering Gateway  
 949-824-9326 [pburke@uci.edu](mailto:pburke@uci.edu)

**Instructor Office Hours:** 2-3:30 P.M. M/W

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|----------------------------|---|-----|
| <b>Grading Components:</b> | Midterm Exam # 1 (Oct. 27)                | 25% |
|                            | Midterm Exam # 2 (Nov. 17)                | 25% |
|                            | Final project                             | 25% |
|                            | Final Exam (Monday, Dec. 8, 2008, 4-6 PM) | 25% |

**Approximate schedule of lectures:**

| Week | M         | W    | Topic             |
|------|-----------|------|-------------------|
| 1    | Ch2       | Ch3  | MOSFET, SS amps   |
| 2    | Ch4       | X    | Differential amps |
| 3    | Ch5       | Ch5  | Current sources   |
| 4    | Ch6       | Ch6  | Freq response     |
| 5    | Midterm 1 | Ch7  | Noise             |
| 6    | Ch7       | Ch7  | Noise             |
| 7    | Ch8       | Ch8  | Feedback          |
| 8    | Midterm 2 | Ch9  | OpAmps            |
| 9    | Ch9       | Ch10 | Stability         |
| 10   | Ch10      | Ch11 | References        |