4.4. Under the generalized scaling rules, $W \rightarrow W / \kappa, L \rightarrow L / \kappa, t_{o x} \rightarrow t_{o x} / \kappa, V_{g} \rightarrow(\alpha / \kappa) V_{g}$, and $V_{t} \rightarrow(\alpha / \kappa) V_{t}$, Eq. (3.79) transforms as

$$
I_{d s a t} \rightarrow \frac{\alpha}{\kappa} C_{o x} W v_{s a t}\left(V_{g}-V_{t}\right) \frac{\sqrt{1+2 \alpha \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right)}-1}{\sqrt{1+2 \alpha \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right)}+1}
$$

In the limit of $\mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right) \gg 1$ (short channel or velocity saturation), the fractional factor in the above equation equals 1 independent of $\alpha$, so $I_{d s a t} \rightarrow(\alpha / \kappa) I_{d s a t}$. On the other hand, if $\alpha \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right) \ll 1$ (long channel), the fractional factor can be approximated by $\alpha \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(2 m v_{s a t} L\right)$, and one has $I_{\text {dsat }} \rightarrow\left(\alpha^{2} / \kappa\right) I_{\text {dsat }}$. In general, the scaling behavior is between the two limits.
4.5. (a)

$$
\begin{aligned}
& V_{t}=V_{f b}+2 \psi_{B}+\frac{q N_{a}}{C_{o x}}\left(W_{d m}-x_{s}\right) \\
& W_{d m}=\sqrt{\frac{4 \mathcal{E}_{s i} \psi_{B}}{q N_{a}}+x_{s}^{2}}
\end{aligned}
$$

Eliminate $N_{a}$ from the above eqs,

$$
\begin{aligned}
& \qquad x_{s}=\frac{4 \varepsilon_{s i} \psi_{B}}{C_{o x}\left(V_{t}-V_{f b}-2 \psi_{B}\right)}-W_{d m} \\
& V_{f b}=-E_{g} / 2 q-\psi_{B}=-1.06 \mathrm{~V} \text {, so } x_{s}=17 \mathrm{~nm} . \\
& \text { Substituting in the } 2^{\text {nd }} \text { eq., } N_{a}=1.3 \times 10^{17} \mathrm{~cm}^{-3} .
\end{aligned}
$$

(b)

$$
\begin{aligned}
& m=1+3 t_{o x} \times W_{d m}=1.21 \\
& \text { Inv. Subth. Slope }=m \times 60 \mathrm{mV} / \text { decade }=73 \mathrm{mV} / \text { decade }
\end{aligned}
$$

(c)

$$
\begin{aligned}
& \lambda=W_{d m}+3 t_{o x}=0.12 \mu \mathrm{~m} \\
& L_{\min } \approx 2 \lambda=0.24 \mu \mathrm{~m}
\end{aligned}
$$

4.6. From Eq. (3.25) with $V_{t}$ replaced by $V_{o n}$, the linear region characteristics of the two parts are:

$$
I_{d s 1}=\mu_{e f f} C_{o x} \frac{W_{1}}{L}\left(V_{g}-V_{o n 1}-\frac{m}{2} V_{d s}\right) V_{d s},
$$

and

$$
I_{d s 2}=\mu_{e f f} C_{o x} \frac{W_{2}}{L}\left(V_{g}-V_{o n 2}-\frac{m}{2} V_{d s}\right) V_{d s} .
$$

Since they are connected in parallel, $V_{g}$ and $V_{d s}$ are the same and the total current is $I_{d s}=I_{d s 1}+$ $I_{d s 2}$. It is straightforward to show that

$$
I_{d s}=\mu_{e f f} C_{o x} \frac{W_{1}+W_{2}}{L}\left(V_{g}-V_{o n}-\frac{m}{2} V_{d s}\right) V_{d s},
$$

where $V_{\text {on }}=\left(W_{1} V_{\text {on } 1}+W_{2} V_{\text {on } 2}\right) /\left(W_{1}+W_{2}\right)$.
4.7. From Eq. (3.23) with $V_{t}$ replaced by $V_{o n}$, the linear region characteristics of the two parts are:

$$
I_{d s}=\mu_{e f f} C_{o x} \frac{W}{L_{1}}\left(V_{g}-V_{o n 1}\right) V_{d s 1},
$$

and

$$
I_{d s}=\mu_{e f f} C_{o x} \frac{W}{L_{2}}\left(V_{g}-V_{o n 2}\right) V_{d s 2} .
$$

Here the second order terms in $V_{d s 1}$ and $V_{d s 2}$ have been neglected (low-drain bias). Since they are connected in series, $I_{d s}$ are the same and the total voltage is $V_{d s}=V_{d s 1}+V_{d s 2}$. Therefore,

$$
V_{d s}=\frac{I_{d s}}{\mu_{e f f} C_{o x} W}\left(\frac{L_{1}}{V_{g}-V_{o n 1}}+\frac{L_{2}}{V_{g}-V_{o n 2}}\right) .
$$

Since $V_{o n 1} \approx V_{o n 2}$, one considers only the first-order terms of $\left(V_{o n 1}-V_{o n 2}\right)$ by letting $V_{o n 2}=V_{o n 1}+$ $\delta$ and expanding the second term in a power series of $\delta$ :

$$
V_{d s} \approx \frac{I_{d s}}{\mu_{e f f} C_{o x} W}\left(\frac{L_{1}+L_{2}}{V_{g}-V_{o n 1}}+\frac{L_{2}}{\left(V_{g}-V_{o n 1}\right)^{2}} \delta\right)=\frac{I_{d s}}{\mu_{e f f} C_{o x} W} \frac{L_{1}+L_{2}}{V_{g}-V_{o n 1}}\left(1+\frac{L_{2} /\left(L_{1}+L_{2}\right)}{V_{g}-V_{o n 1}} \delta\right) .
$$

This, in turn, can be approximated by

$$
V_{d s} \approx \frac{I_{d s}}{\mu_{e f f} C_{o x} W} \frac{\left(L_{1}+L_{2}\right) /\left(V_{g}-V_{o n 1}\right)}{1-\frac{L_{2} /\left(L_{1}+L_{2}\right)}{V_{g}-V_{o n 1}} \delta}=\frac{I_{d s}}{\mu_{e f f} C_{o x} W} \frac{L_{1}+L_{2}}{V_{g}-V_{o n 1}-\left(V_{o n 2}-V_{o n 1}\right) L_{2} /\left(L_{1}+L_{2}\right)},
$$

which can be written as

$$
V_{d s}=\frac{I_{d s}}{\mu_{e f f} C_{o x} W} \frac{L_{1}+L_{2}}{V_{g}-V_{o n}},
$$

where the equivalent linear threshold voltage is $V_{\text {on }}=\left(L_{1} V_{\text {on } 1}+L_{2} V_{o n 2}\right) /\left(L_{1}+L_{2}\right)$.

The energy dissipated in $R$ is

$$
E=\int_{0}^{\infty} R I^{2} d t=\frac{V_{d d}^{2}}{R} \int_{0}^{\infty} e^{-2 t / R C} d t=\frac{1}{2} C V_{d d}^{2}
$$

which is independent of $R$. The same amount of energy is stored in $C$.
If the voltage source is now switched to 0 , one has

$$
V(t)+R C \frac{d V}{d t}=0
$$

With the initial condition $V(t=0)=V_{d d}$, the solution for $V(t)$ is

$$
V(t)=V_{d d} e^{-t / R C}
$$

The energy stored in $C$ is now all dissipated in $R$ :

$$
E=\int_{0}^{\infty} R I^{2} d t=\frac{V_{d d}^{2}}{R} \int_{0}^{\infty} e^{-2 t / R C} d t=\frac{1}{2} C V_{d d}^{2}
$$

5.3. From Eq. (3.25) and the inversion charge expression above Eq. (3.58), the transit time is

$$
\tau_{t r}=\frac{W L C_{o x}\left(V_{g}-V_{t}-m V_{d s} / 2\right)}{\mu_{e f f} C_{o x}(W / L)\left(V_{g}-V_{t}-m V_{d s} / 2\right) V_{d s}}=\frac{L^{2}}{\mu_{e f f} V_{d s}}
$$

for a MOSFET device biased in the linear region.
From Eq. (3.28) and the inversion charge expression above Eq. (3.60), the transit time is

$$
\tau_{t r}=\frac{(2 / 3) W L C_{o x}\left(V_{g}-V_{t}\right)}{\mu_{e f f} C_{o x}(W / L)\left(V_{g}-V_{t}\right)^{2} / 2 m}=\frac{4 m L^{2}}{3 \mu_{e f f}\left(V_{g}-V_{t}\right)}
$$

for a long-channel MOSFET biased in saturation.
5.4. From Eq. (3.79) and the inversion charge expression in Exercise 3.10, the transit time is

$$
\tau_{t r} \equiv \frac{Q_{i}}{I_{d s a t}}=\frac{L}{v_{s a t}} \frac{\sqrt{1+2 \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right)}+1 / 3}{\sqrt{1+2 \mu_{e f f}\left(V_{g}-V_{t}\right) /\left(m v_{s a t} L\right)}-1}
$$

for a short-channel MOSFET biased in saturation. The limiting value of $\tau_{t r}$ is $L / v_{s a t}$ when the device becomes fully velocity saturated as $L \rightarrow 0$.
5.5. The transmission line model of contact resistance in a planar geometry is represented by the distributed network below. The current flows from a thin resistive film (diffusion with a sheet resistivity $\rho_{s d}$ ) into a ground plane (metal) with an interfacial contact resistivity $\rho_{c}$ between them (Fig. 5.16).


Following a similar approach as in Eqs. (5.23)-(5.25), one can write

$$
V(x+d x)-V(x)=\frac{d V}{d x} d x=-I(x) R d x
$$

and

$$
I(x+d x)-I(x)=\frac{d I}{d x} d x=-V(x) G d x .
$$

Here $R=\rho_{s d} / W$ and $G=W / \rho_{c}$. From the above two equations, one obtains

$$
\frac{d^{2} f}{d x^{2}}=R G f=\frac{\rho_{s d}}{\rho_{c}} f
$$

where $f(x)=V(x)$ or $I(x)$.
5.6. The solution to the second-order differential equation in the above exercise is of the $\sinh \left[\left(\rho_{s d} / \rho_{c}\right)^{1 / 2} x\right]$ and $\cosh \left[\left(\rho_{s d} / \rho_{c}\right)^{1 / 2} x\right]$ form. With the boundary condition $I\left(x=l_{c}\right)=0$ where $x=$ 0 is the leading edge and $x=l_{c}$ is the far end of the contact window (see figure in Exercise 5.5), the solution is

$$
I(x)=I_{0} \sinh \left[\sqrt{\frac{\rho_{s d}}{\rho_{c}}}\left(l_{c}-x\right)\right],
$$

where $I_{0}$ is a constant multiplying factor. From the second equation in Exercise 5.5, the voltage is

$$
V(x)=-\frac{\rho_{c}}{W} \frac{d I}{d x}=\frac{\sqrt{\rho_{c} \rho_{s d}}}{W} I_{0} \cosh \left[\sqrt{\frac{\rho_{s d}}{\rho_{c}}}\left(l_{c}-x\right)\right] .
$$

The contact resistance is then

$$
R_{c o}=\frac{V(x=0)}{I(x=0)}=\frac{\sqrt{\rho_{c} \rho_{s d}}}{W} \operatorname{coth}\left[l_{c} \sqrt{\frac{\rho_{s d}}{\rho_{c}}}\right] .
$$

5.7. From Eqs. (5.43) and (5.45), $\tau_{b \min }<\tau$ if

$$
C_{\text {out }}+C_{L}>2 C_{\text {out }}+2 \sqrt{C_{\text {in }} C_{L}} .
$$

This inequality is quadratic in $C_{L}^{1 / 2}$, which can be solved to yield

$$
C_{L}>\left(\sqrt{C_{\text {in }}}+\sqrt{C_{\text {in }}+C_{\text {out }}}\right)^{2} .
$$

Under this condition, the insertion of one or a few properly designed buffer stage(s) will help reduce the overall delay.
5.11.

Power $=C V_{d d}^{2} f \quad$ where $f=1 /(2 \tau) \quad$ and $C=C_{\text {in }}+C_{\text {out }}+C_{L}$
So $\quad$ Power $=V_{d d}{ }^{2} / 2 R_{s w}$

If $\quad\left(W_{n}, W_{p}\right) \rightarrow\left(k W_{n}, k W_{p}\right)$,
$R_{s w} \rightarrow R_{s w} / k$
$C_{\text {in }}+C_{\text {out }} \rightarrow k\left(C_{\text {in }}+C_{\text {out }}\right)$
Therefore,
Delay $\tau \rightarrow R_{\text {sw }}\left(C_{i n}+C_{\text {out }}+C_{L} / k\right)$
Power $\rightarrow k$ times original value

Problem \#3

$$
\binom{i_{g}}{i d}=\left(\begin{array}{ll}
y_{11} & y_{12} \\
y_{21} & y_{22}
\end{array}\right)\binom{v_{g}}{v_{d}}
$$

equiv.to


Compare to HW prob. \#3 circuit diagram $\Rightarrow$

$$
\begin{aligned}
& y_{12}=0 \quad y_{21}=g_{m} \\
& y_{22}=\frac{1}{r_{0}} \quad y_{11}=\left(i w c_{g s}\right)+\frac{1}{r_{1 \text { leak }}}
\end{aligned}
$$

From table $\quad h_{21}=\frac{y_{21}}{y_{1}}$

$$
\Rightarrow h_{21}=\frac{g_{m}}{i w c_{s s}+\frac{1}{r_{\text {leak }}}}
$$

If $V_{\text {leak large }} \Rightarrow \quad i \omega \mathrm{Cg}_{s} \gg \frac{1}{r_{\text {leak }}}$

$$
\Rightarrow h_{21} \approx \frac{g m}{i \omega c_{g s}} \Rightarrow F_{T} \cong \frac{g_{m}}{2 \pi c_{g s}}
$$

$F_{\text {max }}=\infty$. why? At any frequency we can design an impedance matching network at the iuputand output to achier power gan. Therefore there is no upper limit on frequency for power gain. Antherwa to see this is to calculate $U=\frac{\left|y_{2}-y_{r-}\right|^{2}}{g_{m}}$


