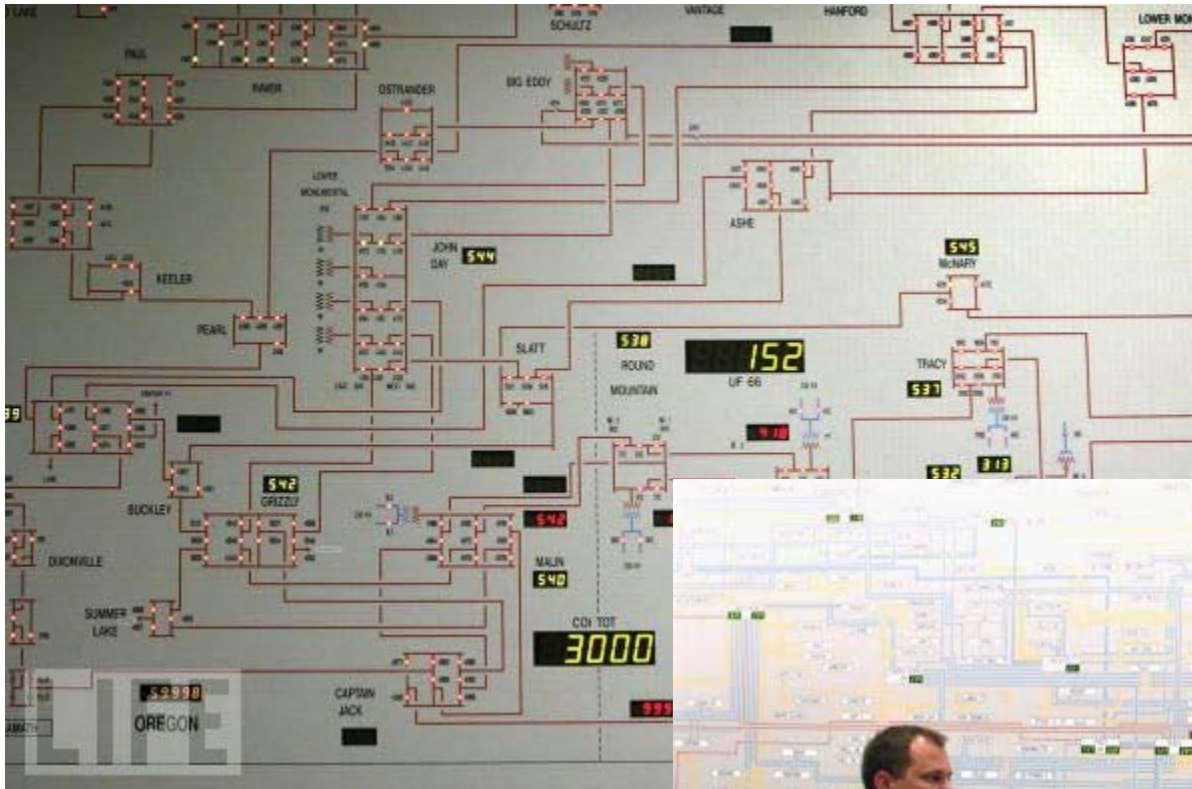
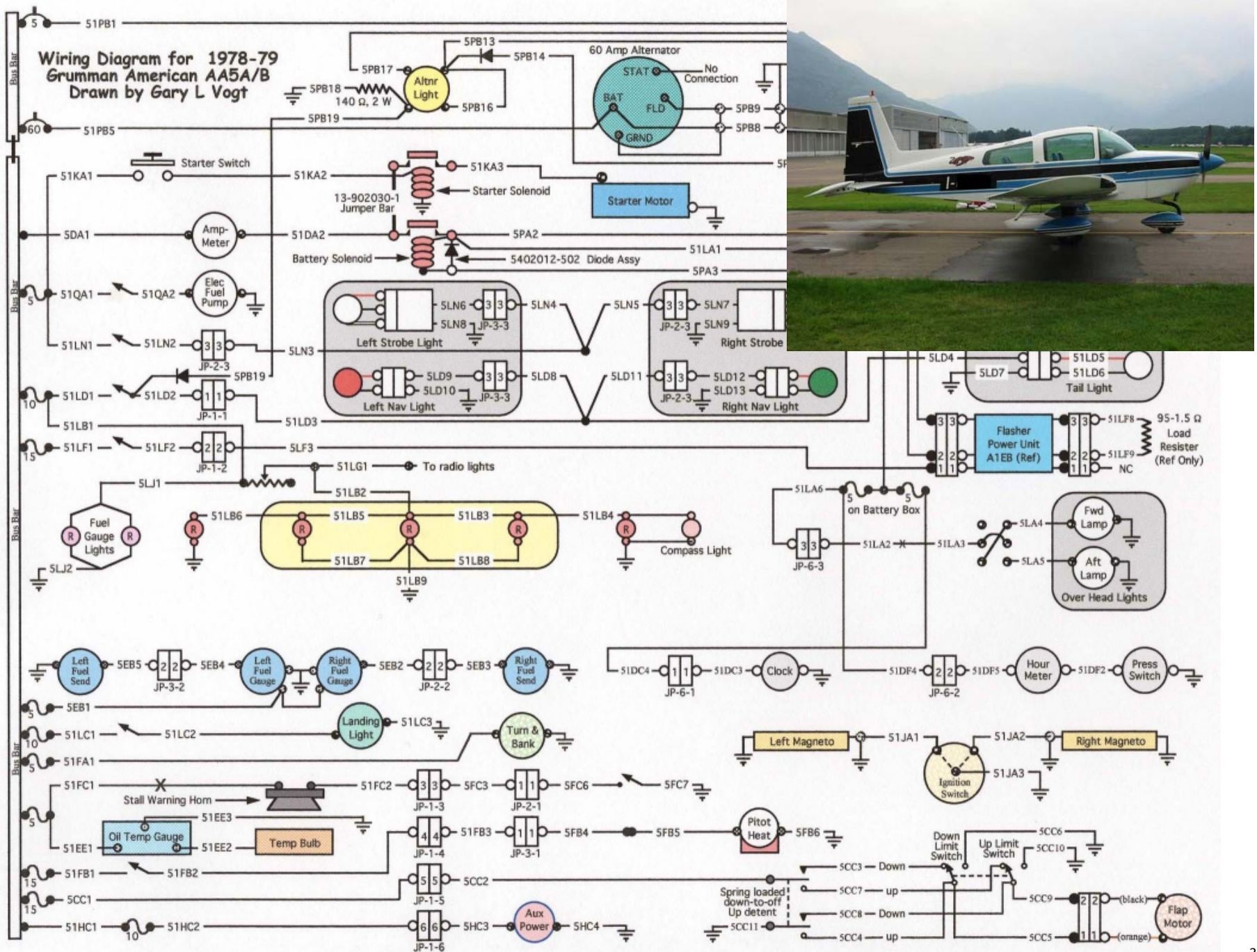


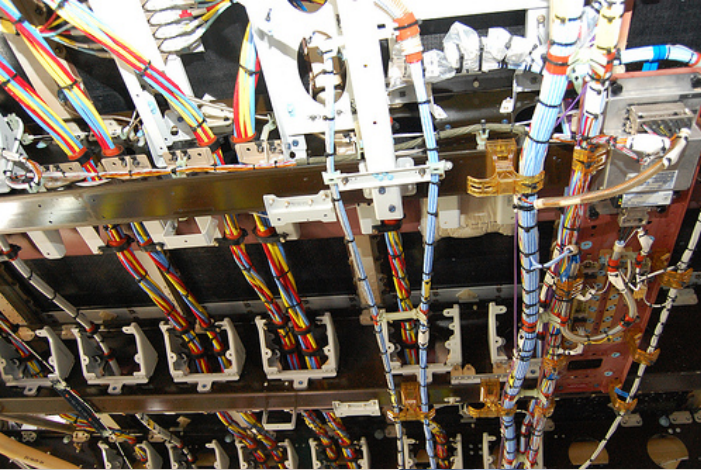
- Announcements:
1. Announcements

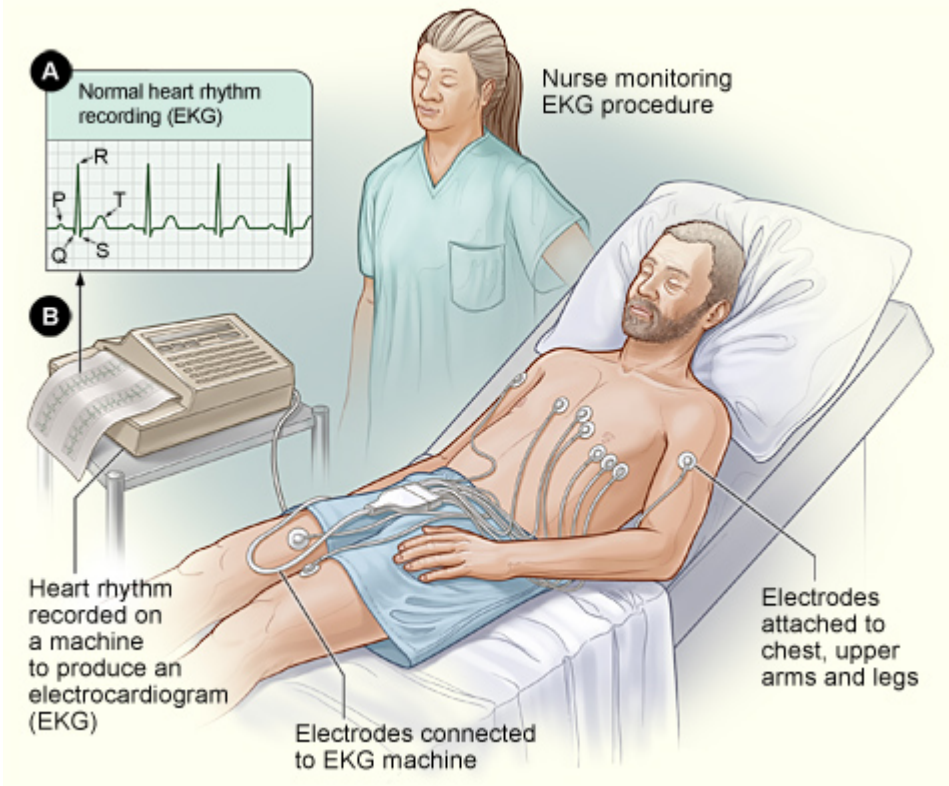
EECS 70A: Network Analysis

Lecture 5







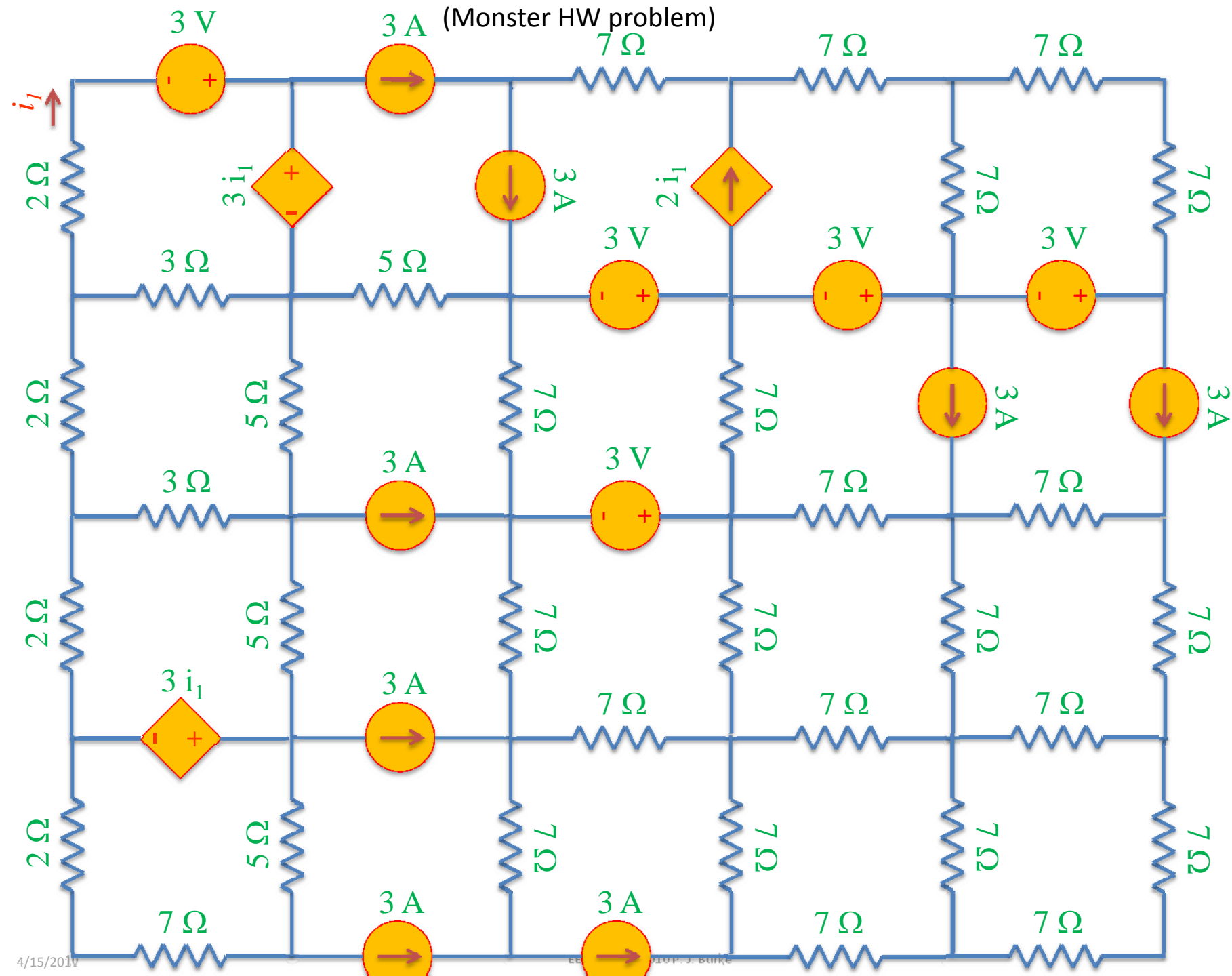


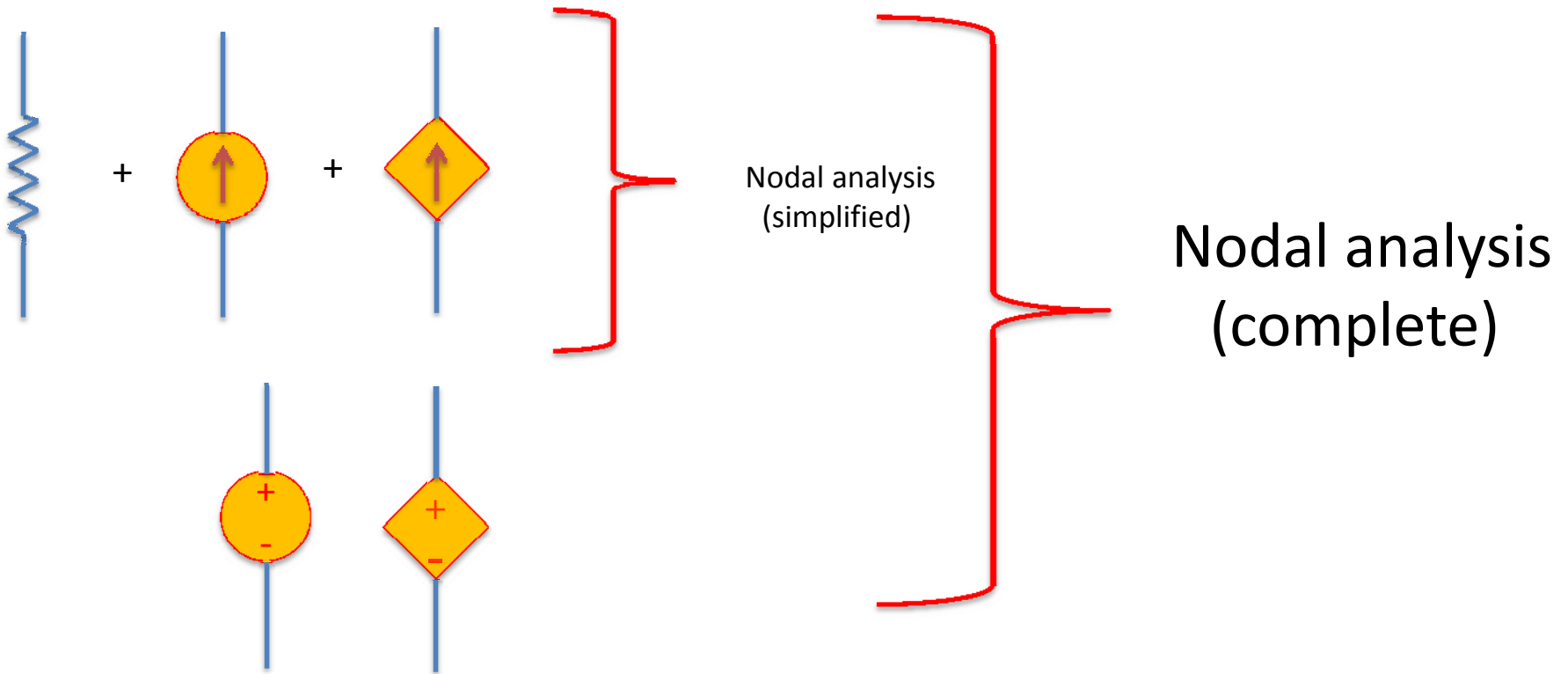


To Comply
Standards
ICE USE

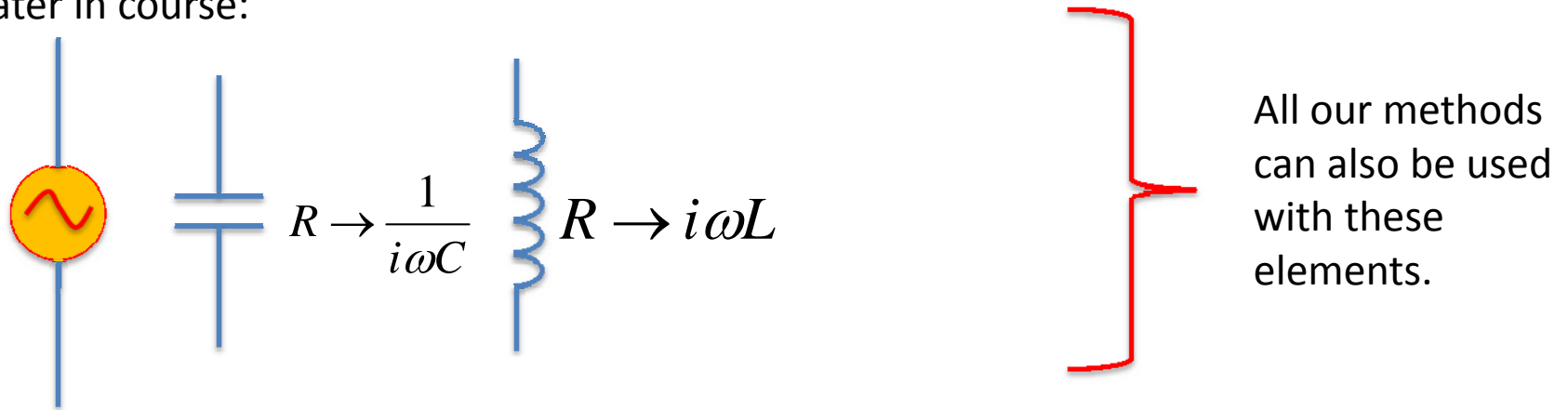


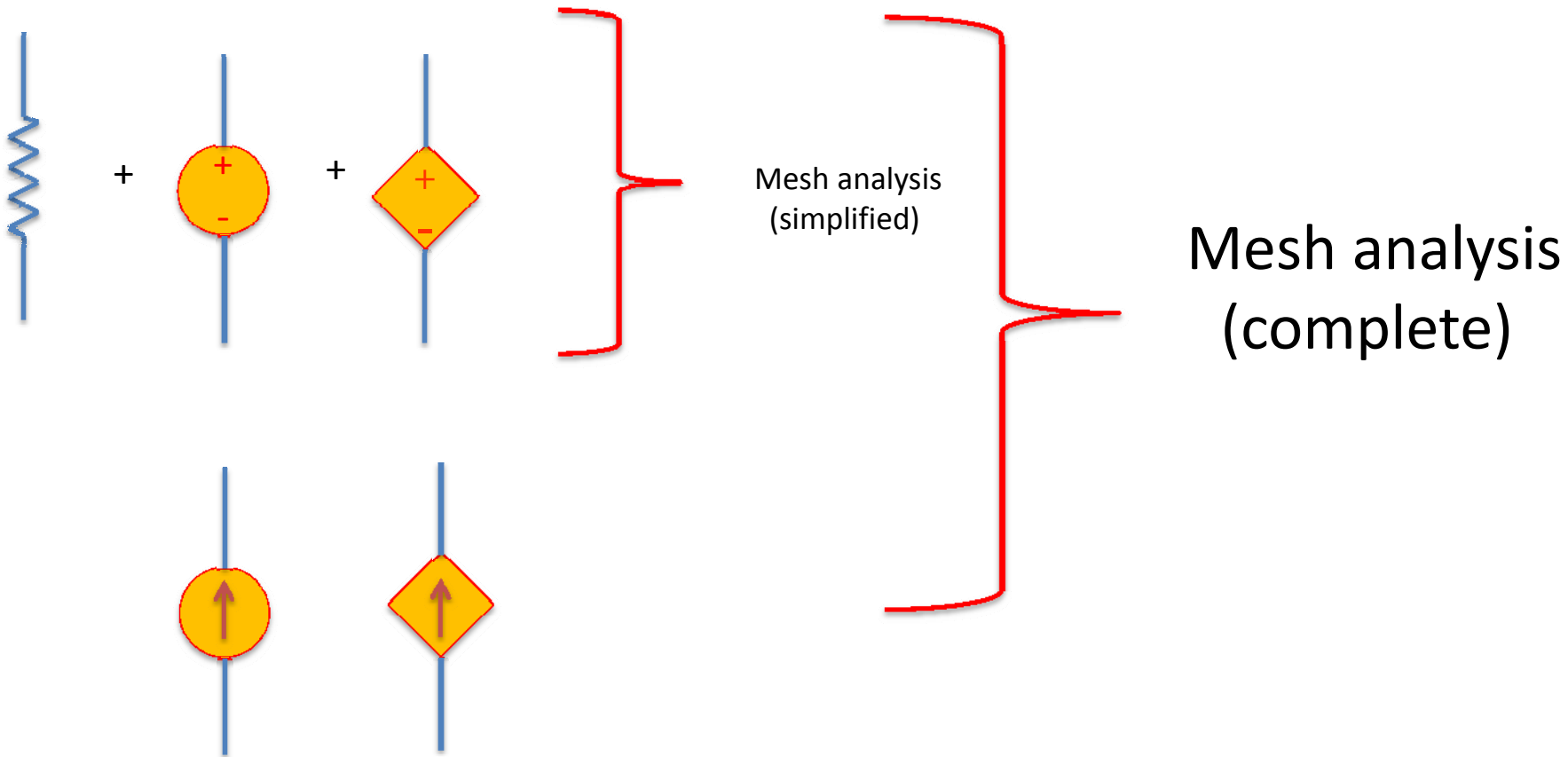
(Monster HW problem)





Later in course:





Diode



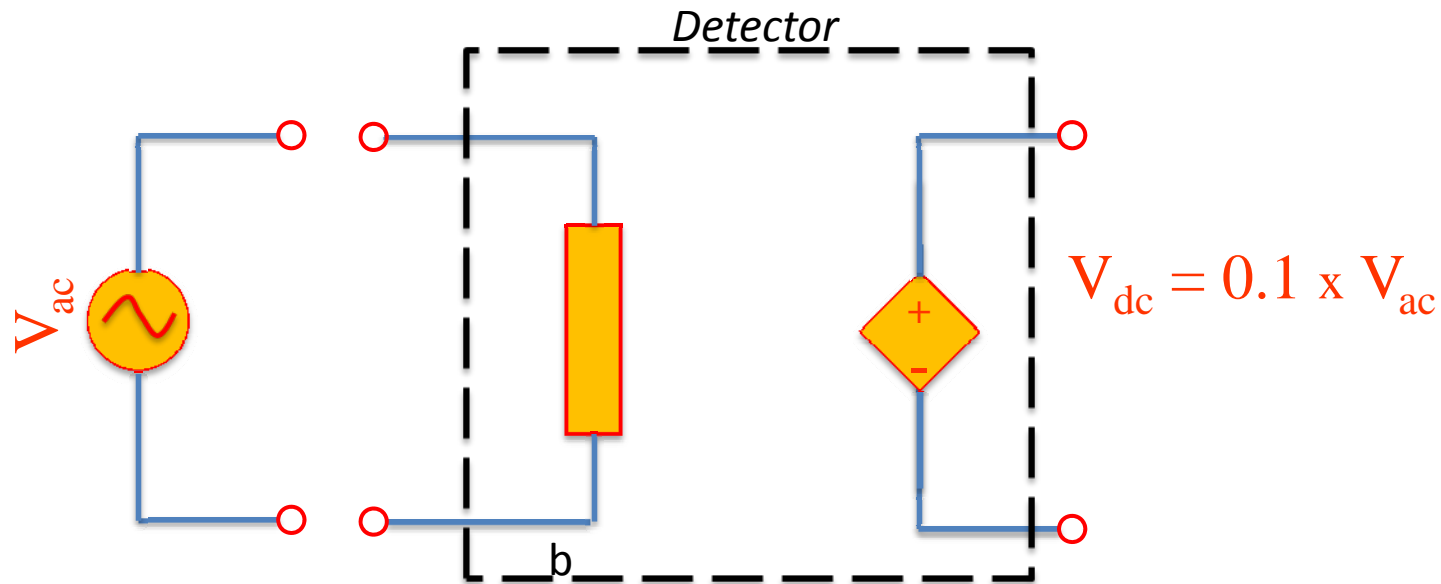
$$I = I_0 \left(e^{qV/kT} - 1 \right)$$

Nodal/mesh analysis won't work on this but...

Solar cell is a diode.
Behaves like:

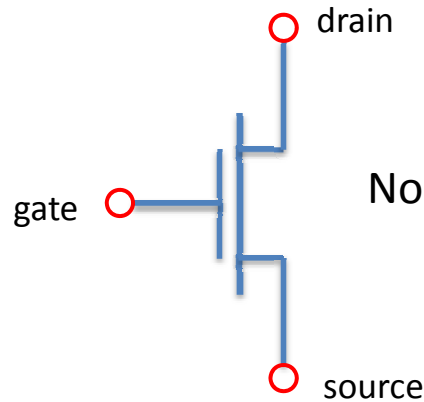


Radio wave detector is a diode.
Behaves like:



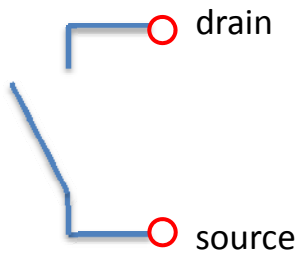
Nodal/mesh analysis works on these equivalent circuits!

Transistors



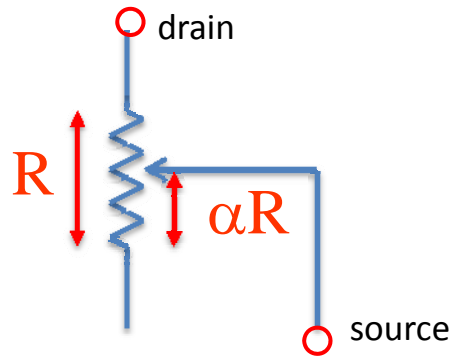
Nodal/mesh analysis won't work on this but...

In some cases behaves like *switch*:



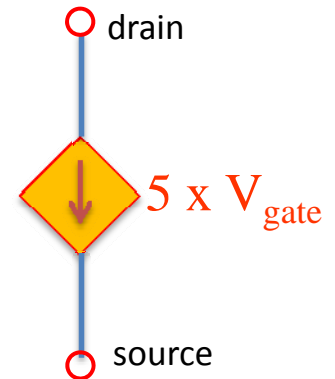
V_{gate}	Switch
+ 5 V	Open
0 V	Closed

In some cases behaves like *potentiometer*:



$$R_{sd} = (\text{constant}) \times V_{gate}$$

In some cases behaves like *VCCS*:



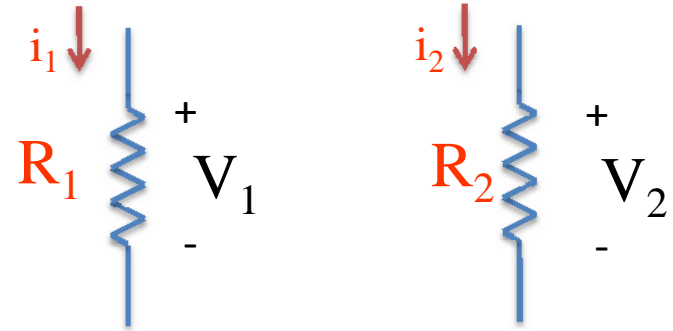
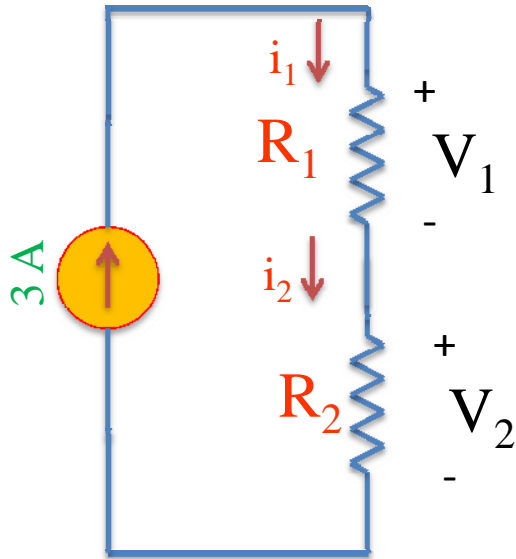
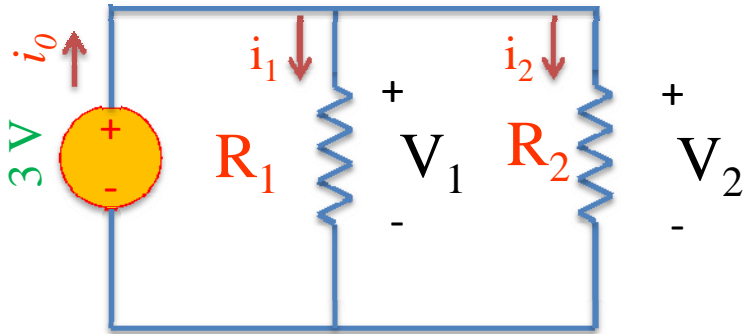
Nodal/mesh analysis works on these equivalent circuits!

Nodal vs. mesh analysis

Consider 2 examples, each with 2 resistors.

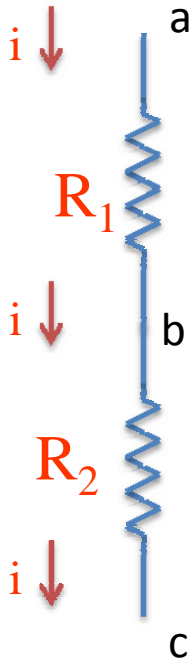
For both problems, find i_1 , i_2 , V_1 , V_2 .

Is it easier to solve for the currents or the voltages first?



Notation: two elements in series

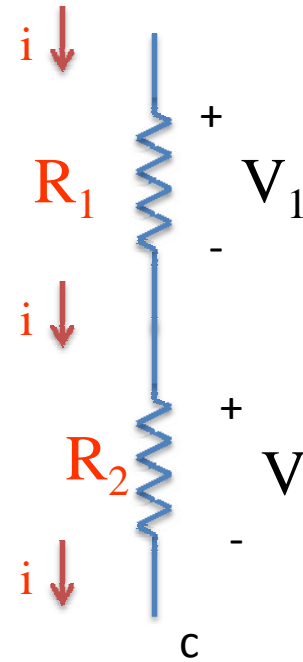
Textbook chapter 2 notation:



$$i = \frac{V_{ab}}{R_1}$$

$$i = \frac{V_{bc}}{R_2}$$

V_{ab} is the voltage drop across resistor 1



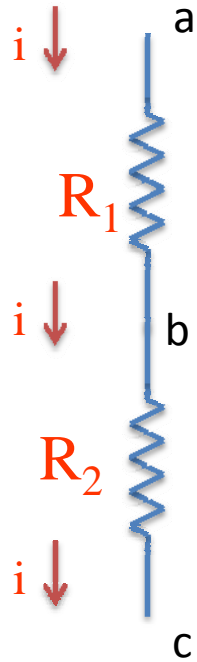
$$i = \frac{V_1}{R_1}$$

$$i = \frac{V_2}{R_2}$$

V_1 is the voltage drop across resistor 1

Nodal analysis

1. Define a reference node.
2. Label remaining nodes

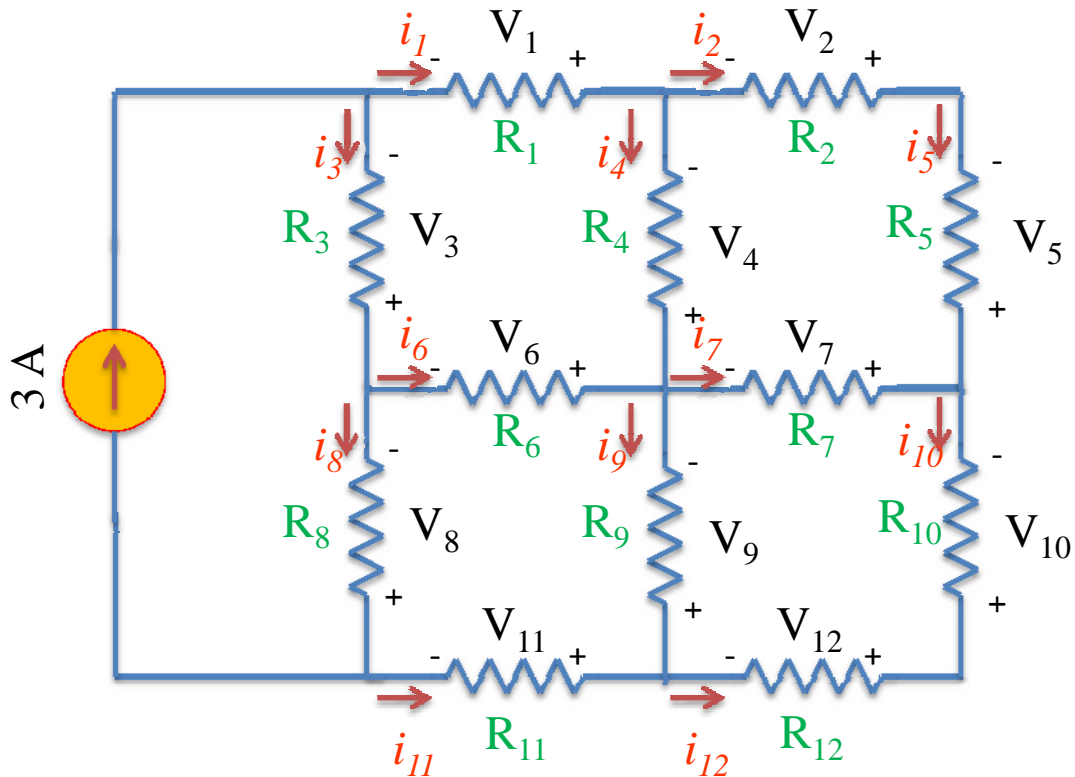


$$i = \frac{V_{ab}}{R_1}$$

$$i = \frac{V_{bc}}{R_2}$$

V_{ab} is the voltage drop
across resistor 1

Example (Ch. 2 notation)



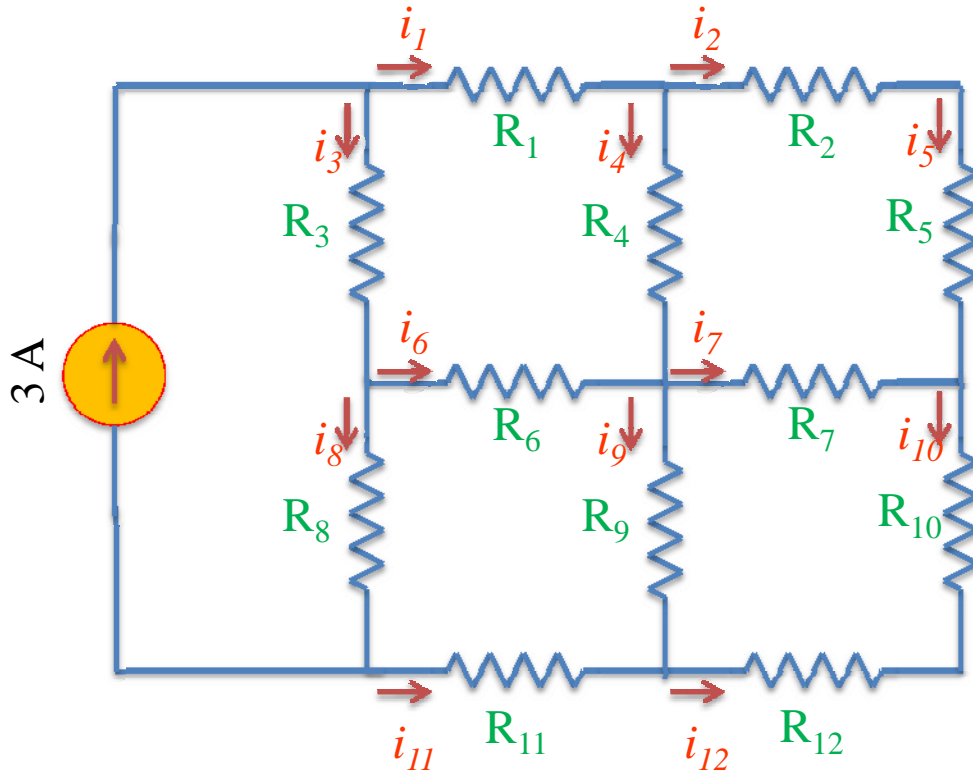
Label voltages as drops across resistors.

Typical notation:

V_1 is voltage drop across R_1 .
 i_1 is current through R_1 .

Same circuit: Nodal analysis

1. Define a reference node.
2. Label remaining nodes.
3. Apply KCL + ohm.



Typical notation:

i_1 is current through R_1 . (Same as before)

V_1 is voltage of node 1 relative to reference node. (Different from before)

Kramer's rule

Solve for x, y, z in terms of known constants $a_{1-3}, b_{1-3}, c_{1-3}, d_{1-3}$:

$$a_1x + b_1y + c_1z = d_1$$

$$a_2x + b_2y + c_2z = d_2$$

$$a_3x + b_3y + c_3z = d_3$$

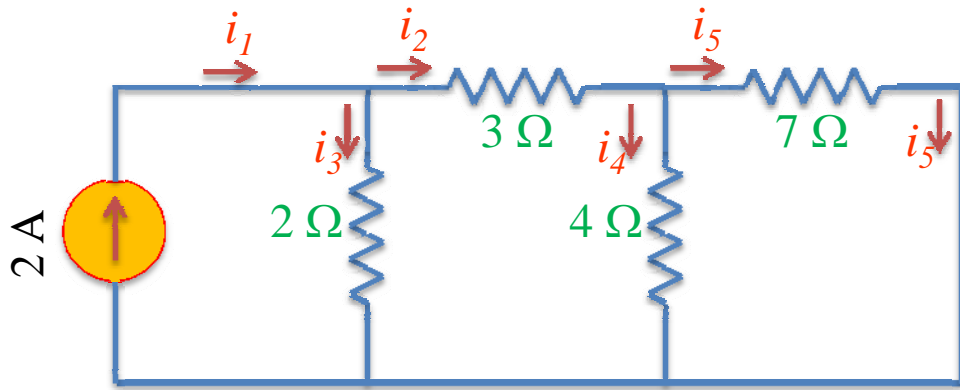
Determinants:

Solve for x, y, z in terms of known constants $a_{1-3}, b_{1-3}, c_{1-3}, d_{1-3}$:

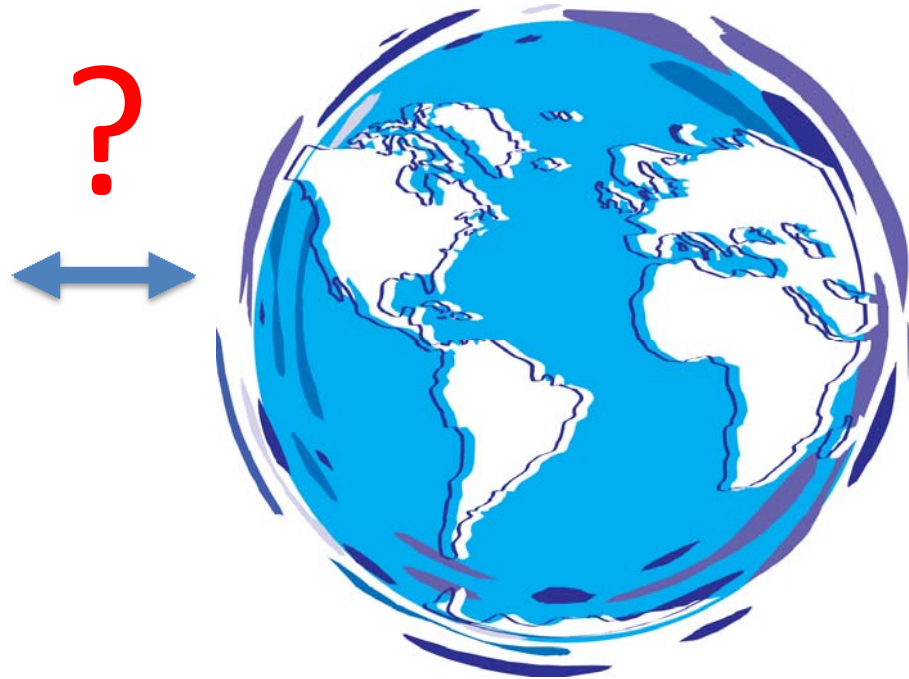
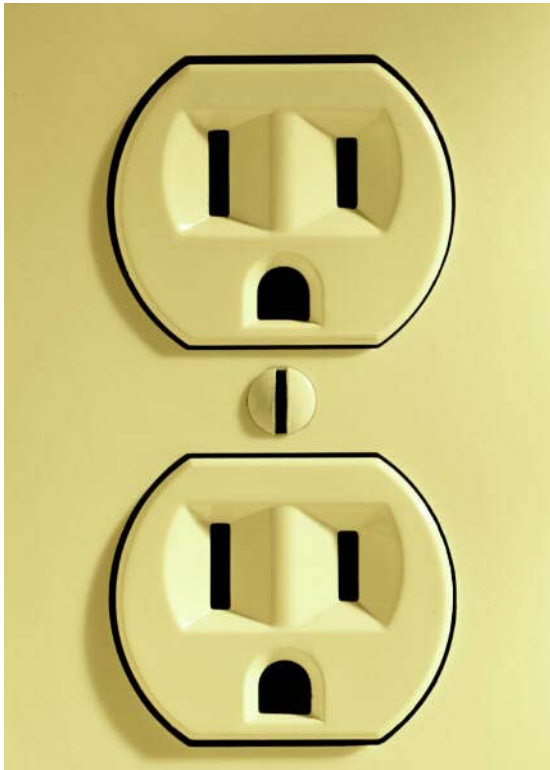
$$\begin{vmatrix} a & b \\ c & d \end{vmatrix} = ab - cd$$

$$\begin{vmatrix} a & b & c \\ d & e & f \\ g & h & i \end{vmatrix} = \dots$$

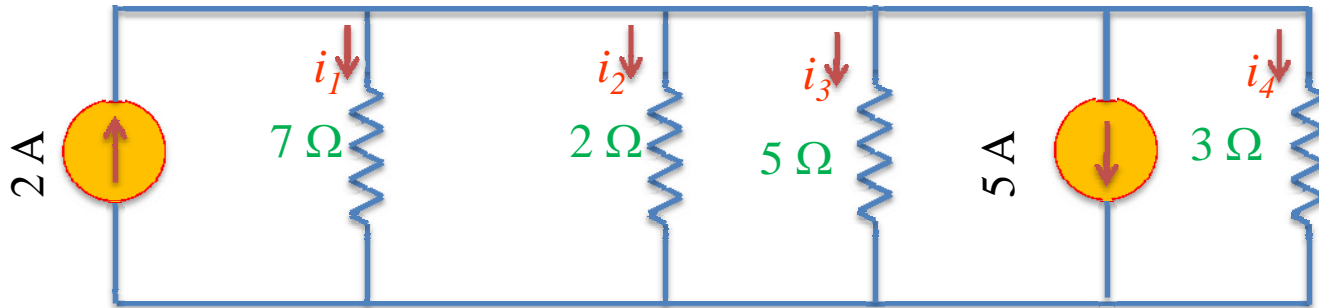
Nodal analysis example



Ground?



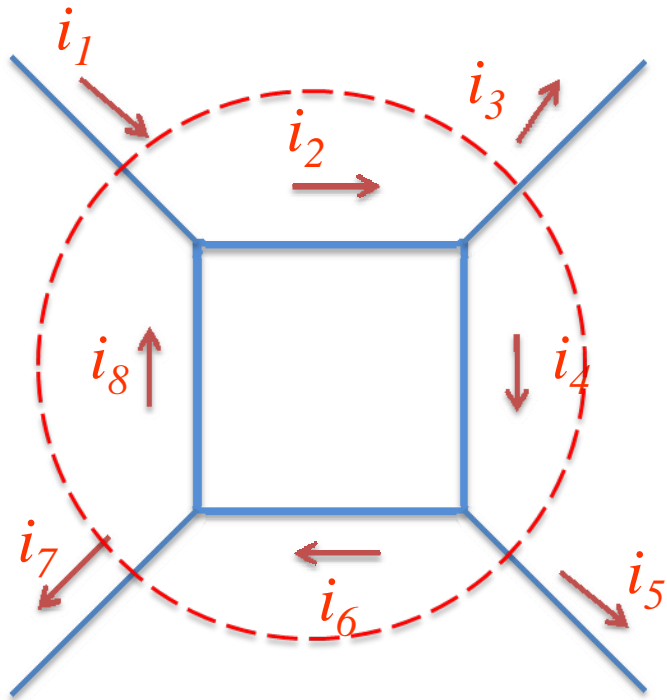
Nodal analysis example



Questions?

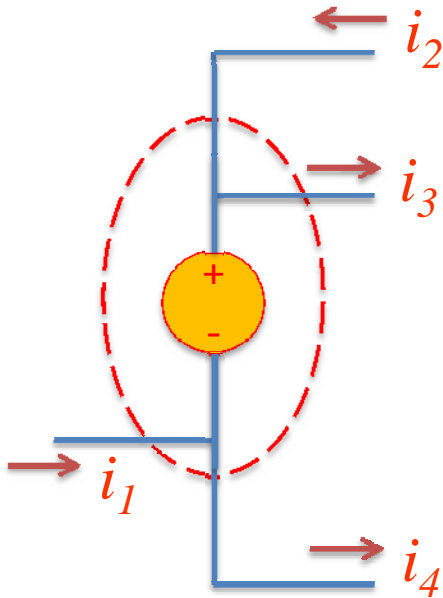
KCL examples

From Lecture 3, Week 2: Find a relationship among $i_1, i_2, i_3, i_4, \dots$



“Supernode”

A node with a voltage source in it...

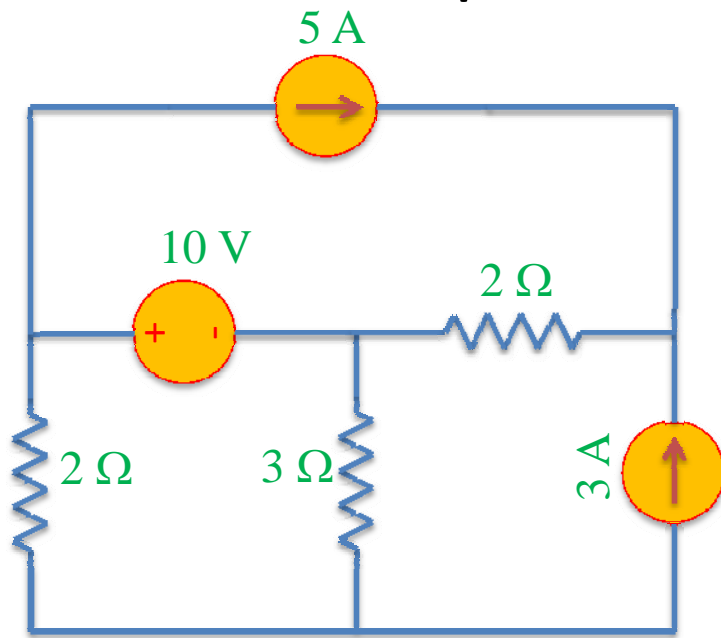


KCL:

Must define a supernode if a voltage source appears when doing nodal analysis...
(unless one end of voltage source connected to reference node)

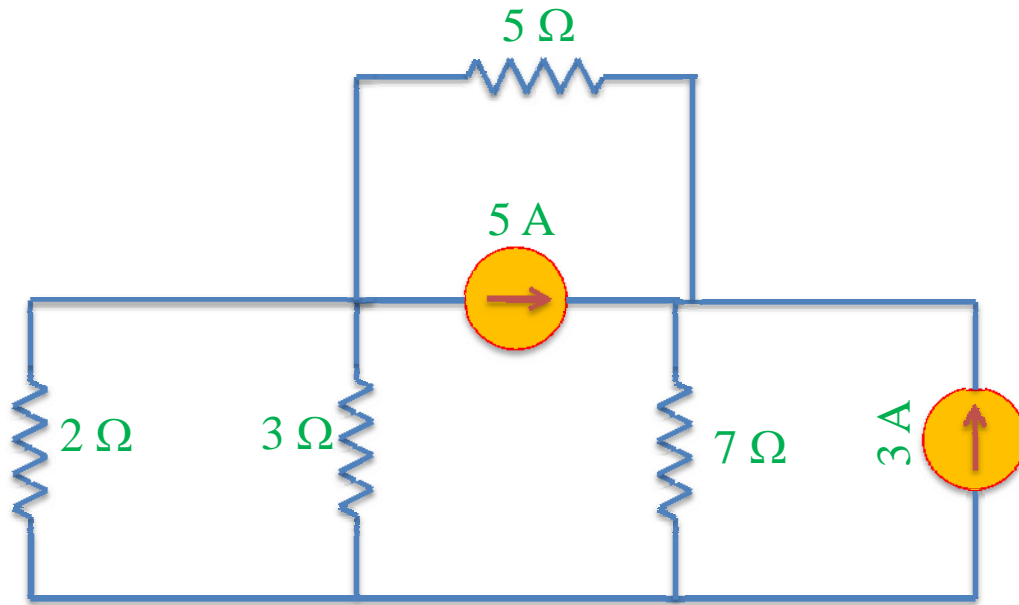
1. Define a reference node.
2. Label remaining nodes.
3. Apply KCL + ohm to all nodes **and supernodes**
4. **Apply KVL to loop with voltage source**

Example nodal w/voltage source

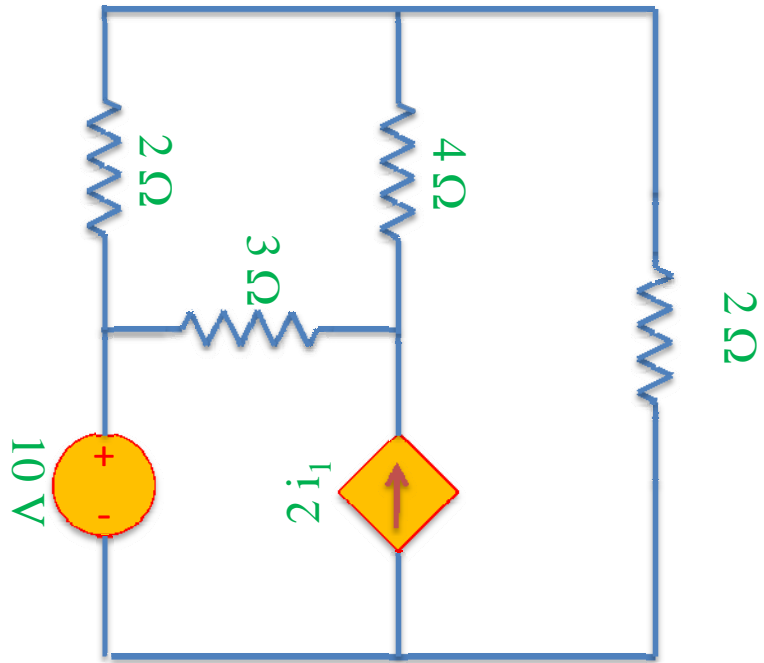


Questions?

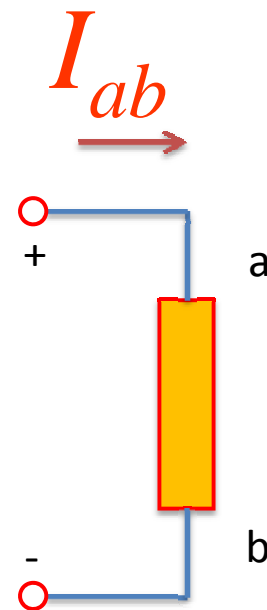
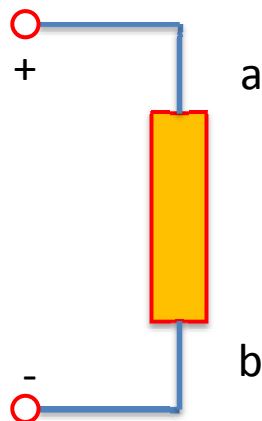
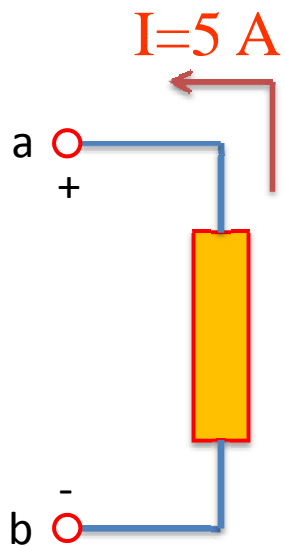
Example nodal w/voltage source



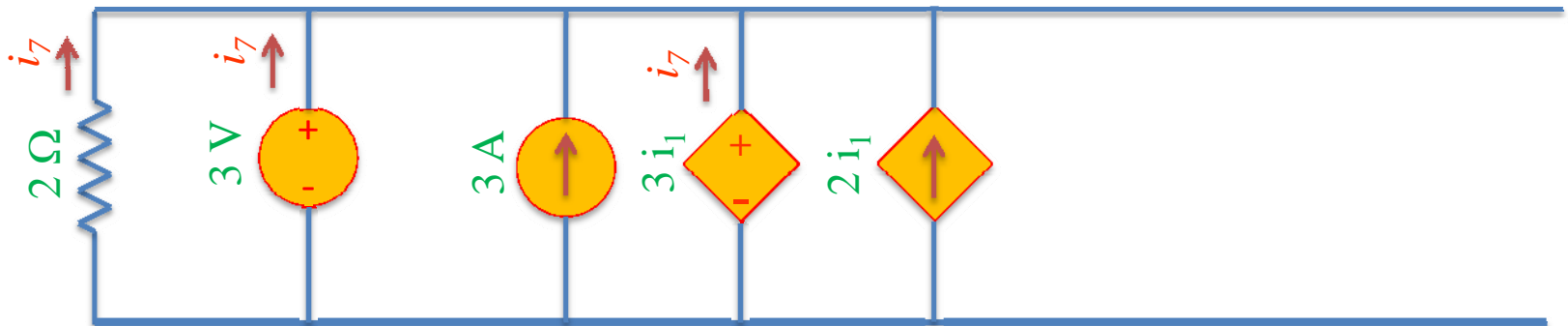
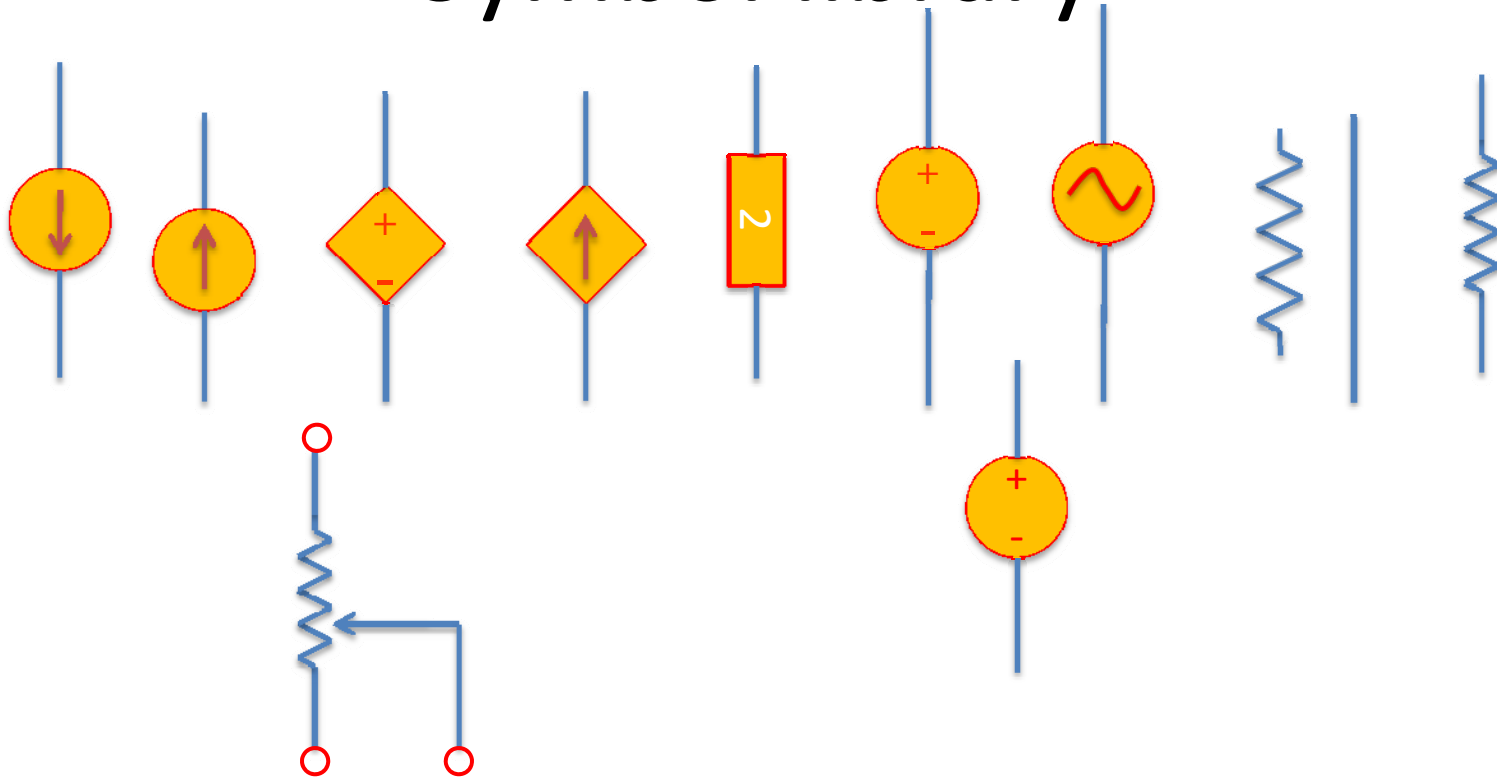
Example nodal w/voltage source



Symbol library



Symbol library



Symbol & circuit library

