Syllabus
EECS 70A (CSE 70A) Spring 2017
Code 16500, 18115, 35890
Network Analysis I

Textbook:	*Fundamentals of Electric Circuits (5th Edition)* Alexander & Sadiku, McGraw Hill, 2013.
Prerequisites:	Physics 7D; EECS10, MAE10, EECS12, CSE41, or ICS31.
Co-requisites:	Mathematics 3D.

What I want you to learn about ("Outcomes"):

1. Use mathematical tools for analyzing linear RLC circuits.

2. Describe the basic network theorems.

3. Describe the concepts of frequency response of linear RLC circuits.

Lecture Hours:	11:00 am – 12:20 pm T/Th
Lecture Classroom:	DBH 1600
Discussion Sessions:	A1: Tu 5:00-5:50 P.M. ELH 110
	A2: Tu 6:00-6:50 P.M. ELH 110
	A3: Th 8:00-8:50 A.M. PCB 1300

Instructor:	Peter Burke, Prof. of Electrical Engineering and Cptr. Science
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Teaching Assistant:	Alireza Karimi Bidhendi, Ali Tazarv, Mahsa Darvishzadeh Varcheie
T.A. Office:	To be announced
T.A. Office Hours:	To be announced

Grading Components:		
	Homeworks	10%
	Weekly quizzes	10%
	Midterm Exams (dates to be announced)	60%
	Final Exam (Tues Jun 13 10:30am-12:30pm)	20%